Complex RF transceivers need to be tested in order to ensure their reliability and functionality.

Testing time and Automatic Test Equipment price are decisive factors in the cost of the final product. Testing during the product’s lifetime is essential to keep its operation within the demanded levels.

Alternative test strategies and methodologies, such as Built-in Self Test, can be explored to avoid the use of expensive equipment.

RF Power Amplifiers are one of the major contributors to the non-linear behaviour of transceivers.

The evaluation of the non-linear behaviour without the need of expensive ATE and performed in-circuit recurring to already available units in RF transceivers (DSP e.g.)

Specified PA output levels need to be satisfied, which requires the presence of automatic gain control when undesirable perturbations are recorded.

These facilities allow monitoring the device during its lifetime.
Description

This thesis proposes a built in-situ adaptive scheme, which allows adjusting the operation of a RF Power Amplifier (PA), namely, to test it for linearity, to ensure output power regulation and to improve its linearity.

It can be implemented taking advantage of digital signal processing resources and A/D conversion units eventually already available in the system, as well as, of the local oscillator, the mixer, the pre-amplifier (VGA) present in RF transceivers to generate and apply test stimuli.

The evaluation of the RF PA linearity resorts to a polynomial fitting methodology. A LMS algorithm is used to obtain, after gathering the corresponding output responses, the coefficients of the best fitting polynomial and from these, the corresponding compression and third order intermodulation points. The second mode allows regulating the output power in order to ensure constant predefined levels regardless, within reasonable limits, thermal drifts, power supply voltage variations, and behavioural faults occurring in the transmission path. The non-linearity correction mode allows improving the PA linearity to the user defined level after adjusting power supply voltage, VGA gain, and transistor gate bias voltage.

Experimental results were obtained with both single tone and digitally modulated stimuli which confirm the validity and applicability of the proposed test and non-linearity correction scheme. Furthermore, a true power detector was developed, having the obtained experimental results confirmed its correct operation and that load impedance variations can be detected more correctly comparing to the peak voltage sensors.
List of publications

- 15 conference and workshop papers, 3 journal papers (1 published, 2 submitted).